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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	Applicant: Jonathan McFarland et al. 2874	
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U.S. PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
<i>Aullah</i>	5,136,359	08/04/1992	Takayama, Y., et al.	357	65	12/19/90
	5,188,702	02/23/1993	Takayama, Y., et al.	156	630	04/23/92
	5,262,226	11/16/1993	Yoshida, Y.	428	209	03/04/91
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**Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
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OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

**Examiner Initial	
<i>Aullah</i>	Delaney, D., et al., "Flip Chip Assembly Utilizing Anisotropic Conductive Films: A Feasibility Study", <u>2000 Electronic Components and Technology Conference</u> , 641-645, (2000)

Examiner Akm E. Ullah Date Considered 4-28-2002

*Substitute Disclosure Statement Form (PTO-1449)

**EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.